



2828

J.H. Fischer 42

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Jonathan H. Fischer
Case: 42
Serial No.: 10/039,321
Filing Date: November 9, 2001
Group: 2828
Examiner: Dung T. Nguyen

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature: Lucas H. Hamlin Date: December 4, 2003

Title: Optical Source Driver with Output
Load Detection Circuit

TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith is the following document relating to the above-identified patent application:

(1) Response to Office Action.

There is no additional fee due in conjunction with the response. In the event of any non-payment or improper payment of a required fee, the Commissioner is hereby authorized to charge or to credit **Ryan, Mason & Lewis, LLP Deposit Account No. 50-0762** as required to correct the error.

Respectfully submitted,

Date: December 4, 2003

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Signature: Laura M. Hanlon Date: December 4, 2003

Title: Optical Source Driver with Output Load Detection Circuit

RESPONSE TO OFFICE ACTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following remarks are submitted in response to the second non-final Office Action dated September 4, 2003 in the above-identified application.

REMARKS

The present application was filed on November 9, 2001 with claims 1-17. Claims 1-17 remain pending. Claims 1, 16 and 17 are the independent claims.

Applicant respectfully requests reconsideration of the present application in view of the remarks below.

Claims 1-17 stand rejected under 35 U.S.C. §112, second paragraph, on the grounds that independent claims 1, 16 and 17: (i) allegedly "fail to recite the structural relationship between the current generator circuit, the input stage, the output stage and the output indicator," and (ii) allegedly do not make clear "what the output load detection circuit is used to detect." Applicant respectfully traverses the §112 rejection.

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